WHAT IS CLAIMED IS:

- A mixer device comprising:
- an amplifier circuit including:
- a first amplifier whose input-to-output
- 5 characteristic indicates a hyperbolic tangent function characteristic;
- a second amplifier whose input-to-output characteristics indicates an exponential characteristic, the second amplifier being connected in parallel to the first amplifier; and

input and output terminals which are common to the first amplifier and the second amplifier; and

- a bias controller configured to control a bias of at least one of the first and second amplifiers; and
- an additional differential amplifier connected between a node of the first amplifier and the second amplifier and the output terminal.
 - A mixer device comprising:
 an amplifier circuit including:

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- a differential amplifier whose input-to-output characteristic indicates a hyperbolic tangent function characteristic and which is configured by a differential pair of transistors connected to a variable current source;
- 25 a common emitter amplifier whose input-to-output characteristic indicates an exponential characteristic

and which is connected in parallel to the differential amplifier and is configured by a pair of common-emitter configuration transistors;

input and output terminals which are common to the differential amplifier and the common emitter amplifier, an input signal being input to the input terminals and an output signal output from the output terminals; and

a bias controller configured to control a bias of at least one of the differential amplifier and the common to emitter amplifier; and

an additional differential amplifier connected between a node of the differential amplifier and the common emitter amplifier and the output terminal.

3. A mixer device comprising:

an amplifier circuit including:

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a differential amplifier whose input-to-output characteristic indicates a hyperbolic tangent function characteristic and which is configured by a differential pair of transistors whose emitters are connected to a variable current source;

a common emitter amplifier whose input-tooutput characteristic indicates an exponential
characteristic and which is connected in parallel to
the differential amplifier and is configured by a
pair of common-emitter configuration transistors

whose emitters are grounded through a variable voltage source;

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input and output terminals which are common to the differential amplifier and the common emitter amplifier, an input signal being input to the input terminals and an output signal from the output terminals; and

a bias controller configured to control a bias
of at least one of the differential amplifier and
the common emitter amplifier; and
an additional differential amplifier connected
between a node of the differential amplifier and the

common emitter amplifier and the output terminal.